

isc Silicon NPN Power Transistor

2SD725

DESCRIPTION

- With TO-3 Package
- · Low collector saturation voltage
- Minimum Lot-to-Lot variations for robust device performance and reliable operation.

APPLICATIONS

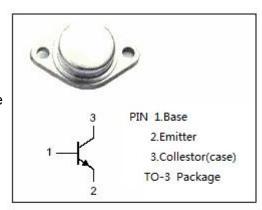
Designed for color TV horizontal deflection driver

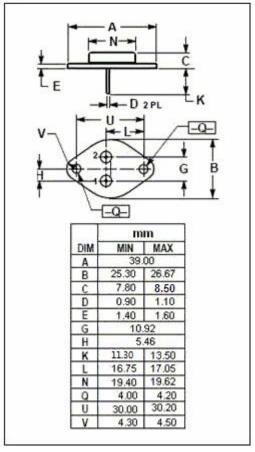
ABSOLUTE MAXIMUM RATINGS(Ta=25℃)

SYMBOL	PARAMETER	VALUE	UNIT
V _{CBO}	Collector-Base Voltage	1500	V
V_{CEO}	Collector-Emitter Voltage	600	V
V _{EBO}	Emitter-Base Voltage	7	V
Ic	Collector Current-Continuous	6	Α
Pc	Collector Power Dissipation @ T _C =25°C	50	W
TJ	Junction Temperature	150	$^{\circ}\!\mathbb{C}$
T _{stg}	Storage Temperature Range	-65-150	$^{\circ}$ C

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	MAX	UNIT
R _{th j-c}	Thermal Resistance, Junction to Case	3.5	°C/W







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ELECTRICAL CHARACTERISTICS

T_C=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	MAX	UNIT
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = 5A; I _B = 0.8A		5.0	V
V _{BE(sat)}	Base-Emitter Saturation Voltage	I _C = 5A; I _B = 0.8A		1.5	V
I _{CBO}	Collector Cutoff Current	V _{CB} =1500V;I _B = 0		1	mA
I _{EBO}	Emitter Cutoff Current	V _{EB} = 10V; I _C =0		10	uA
h _{FE-1}	DC Current Gain	I _C = 1A; V _{CE} = 5V	8	36	
h _{FE-2}	DC Current Gain	I _C = 5A; V _{CE} = 5V	5		
t _f	Fall Time	I _C = 5.0A; I _B = 0.8A		2.0	μ \$



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